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DATE: Thursday, October 20, 2005

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☐	L9	18 same ((based or respons$4 or depend$4) near3 (id or identification or identity or name) near3 (peripheral or device))	5
☐	L8	L7 same (select$4 or choos$)	9663
☐	L7	(plurality or multiple or several) near3 (os or (operating adj system) or (operating adj entity))	65201
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☐	L5	L4 same (os or (operating adj system) or (operating adj entity))	20
☐	L4	interrupt$4 same ((based or respons$4 or depend$4) near3 (id or identification or identity or name) near3 (peripheral or device))	71
☐	L3	L2 same (os or (operating adj system) or (operating adj entity))	439
☐	L2	interrupt$4 same ((based or respons$4 or depend$4) near3 (id or identification or identity or name))	1390
☐	L1	interrupt$4 same (shar$4 near2 interface) same ((based or respons$4 or depend$4) near3 (id or identification or identity or name))	2

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L5: Entry 2 of 20   File: PGPB   May 22, 2003

DOCUMENT-IDENTIFIER: US 20030097503 A1
TITLE: PCI compatible bus model for non-PCI compatible bus architectures

Detail Description Paragraph:
[0039] If any PCI type of devices are found at block 516, the computer proceeds to
set up these devices. At block 518, the PCI system driver receives and recognizes
the device ID and the vendor ID for each of the devices found. Based on the device
ID and the vendor ID, the system can load the appropriate device driver for
specified device at block 520. Device drivers are loaded for each of the devices so
that the devices can operate properly with the system. At block 522, the hardware
for the PCI devices is initialized and configured. Once these I/O devices are
configured, the computer can control and communicate with the devices. The
operating system maps any interrupts needed to the devices at block 524. These
interrupts can be used by the system to request service from a PCI type device and
by a device on the PCI bus to request service from the system. When all the devices
on the PCI bus are recognized and configured, the computer proceeds on towards
normal operations at block 526.

L5: Entry 3 of 20   File: PGPB   · May 30, 2002

DOCUMENT-IDENTIFIER: US 20020065964 A1
TITLE: Method and system for latency-independent peripheral device identification

Abstract Paragraph:
Method and system for latency-independent peripheral device identification. In one
embodiment, a computer system receives an interrupt from a peripheral device
coupled to a computer system communications port. In response, an interrupt
notification message is posted alerting a notification handler running on the
system. It is determined whether the interrupt is indicates peripheral class
compliance. In one embodiment, communications port device sense pin voltage is
determinative. If the interrupt indicates peripheral class compliance and the
communications port is inactive, the port is opened, and inquiry sent to the
peripheral device via the open port. The computer system then waits for response
from the peripheral device. If response is received within a predetermined time,
identification is posted based on the response, including peripheral device
classification information, so that a software handler registered with the
operating system can handle the identification message when received. Thus, this
embodiment imposes no time-critical interrupt response.

Detail Description Paragraph:
[0072] Further, in this embodiment, if a full response, terminated by a CR/LF pair,
is received within the 100 ms, then the HotSync interrupt notification handler
posts an identification notification message, with the ID string of the
peripheral's response as data, to indicate that the peripheral queried has
responded properly. In one embodiment, the identification information is propagated
to a registered handler through a system notification or broadcast mechanism, such
as through the Notification Manager supported by PalmOS version 3.5 or newer.
However, if the character(s) received are not terminated by a CR/LF pair, then a
no-response notification is posted instead. Within the scope of the present
invention, it is up to the developers (of peripheral devices and their drivers, for
example) to register custom-developed, device-specific software handler(s) with the
operating system so that these special handlers will receive identification
notifications corresponding to the peripheral(s) in which they are interested
(e.g., those responses which contain the matching peripheral ID(s) in the data
string) and handle them accordingly. Failing to do so would cause the
identification notification to be handled in a default manner, as described below.